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26314
11-27-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of: G. DIWAKAR VISHAKHADATTA ET AL.

Filed: MARCH 29, 2001

For: DIGITAL INTERFACE IN RADIO-FREQUENCY APPARATUS AND ASSOCIATED METHODS

Serial No.: 09/821,340

Group Art Unit: 2631

Examiner: UNKNOWN

Atty Dkt: SILA:073

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Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on the date below:

11-11-02 Marty Bower
Date Name

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document(s) listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R. §§ 1.97(g),(h), this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

The present Supplemental Information Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be

timely filed in accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with the filing of this Supplemental Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Commissioner is hereby authorized to deduct said fees from Deposit Account No. 10-1205/SILA:073.

A copy of the listed document(s) required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.

Applicant respectfully requests that the listed document(s) be made of record in the present case.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'MR Peterson', written over a horizontal line.

Maximilian R. Peterson
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Attorney for Applicant

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Enclosures

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List of Patents and Publications for Applicant's

Applicants

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	C23	Analog Devices, AD20msp415, GSM/DCS1800/PCS1900, Baseband Processing Chipset, Rev. O, 1997, pp. 1-7.
	C24	Kwentus et al., "A Single-Chip Universal Digital Satellite Receiver With 480-MHz IF Input," IEEE Journal of Solid-State Circuits, Vol. 34, No. 11, November 1999, pp. 1634-1646.
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	C40	Texas Instruments, TRF6901, "Single Chip RF Transceiver," March 2002, pp. 1-29.
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	C43	Philips Semiconductor, "Bluetooth RF Transceiver," Data Sheet, UAA3558, December 21, 2000, pp. 1-5.
	C44	Philips Semiconductor, "Image Reject 1 800 MHz Transceiver For DECT Applications," Data Sheet, UAA2067G, October 22, 1996, pp. 1-24.
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	C49	Fague, "Othello: A New Direct-Conversion Radio Chip Set Eliminates IF Stages," Analog Dialogue 33-10, 1999, pp. 1-3.
	C50	Analog Devices, AD6523/AD6524, "GSM Direct Conversion Radio Chip Set," 1999, 2 pgs.
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	C54	Ericsson, "RF Transceiver Circuit For The Digital Enhanced Cordless Telecommunications (DECT) System," PBL40215, January 2001, pp. 1-22.
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	C65	Crols et al., "An Analog Integrated Polyphase Filter For A High Performance Low-IF Receiver," Symposium On VLSI Circuits Digest Of Technical Papers, 1995, pp. 87-88.
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	C69	Sedra et al., "Complex Analog Bandpass Filters Designed By Linearly Shifting Real Low-Pass Prototypes," IEEE International Symposium On Circuits And Systems, Vol. 3, 1985, 5 pgs.
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	C71	Rudell, et al., "Second Generation Multi-Standard Monolithic CMOS RF Transceiver," University of California, Berkeley, Slides 1 through 9 (June 1996)
	C72	Cho, et al., "Multi-Standard Monolithic CMOS RF Transceiver," University of California, Berkeley, Slides 1 through 26 (June 1996)
	C73	Copending U.S. Patent Application Serial No. 09/821,342, filed March 29, 2001, "Partitioned Radio-Frequency Apparatus And Associated Method" (Sila:072)
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	C75	Copending U.S. Patent Application Serial No. 10/075,094, filed February 13, 2002, "Radio-Frequency Communication Apparatus And Associated Methods" (Sila:074)
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	C77	Copending U.S. Patent Application Serial No. 10/075,122, filed February 12, 2002, "Digital Architecture For Radio-Frequency Apparatus And Associated Methods" (Sila:078)

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	C84	Copending U.S. Patent Application Serial No. 10/081,730, filed February 22, 2002, "Method And Apparatus For Synthesizing High-Frequency Signals For Wireless Communications" (Sila:106)
	C85	Copending U.S. Patent Application Serial No. 10/079,057, filed February 19, 2002, "Apparatus And Method For Front-End Circuitry In Radio-Frequency Apparatus" (Sila:107)
	C86	Allen, "Complex Analog Filters Obtained From Shifted Lowpass Prototypes," September 1985, 118 pgs.
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